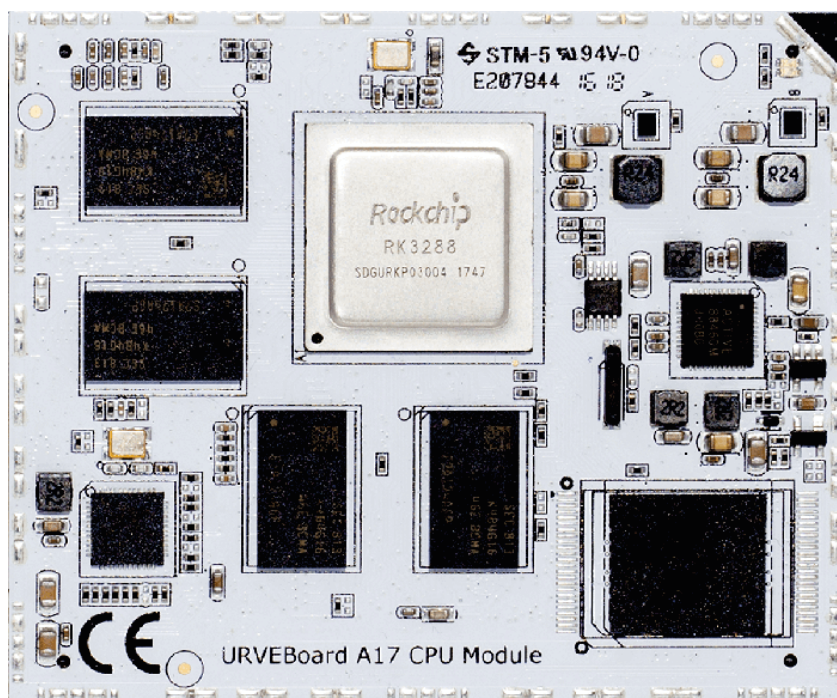


URVE CoreBoard A17 mini3288 Hardware User Manual



1. Introduction

1.1. About this Manual

This manual is intended to provide the user with an overview of the board and benefits, complete features specifications, and set up procedures. It contains important safety information as well.

1.2. Feedback and Update to this Manual

To help our customers make the most of our products, we are continually making additional and updated resources available on the eveo website (eveo.pl).

These include manuals, application notes, programming examples, and updated software and hardware. Check in periodically to see what's new!

When we are prioritizing work on these updated resources, feedback from customers is the number one influence. If you have questions, comments, or concerns about your product or project, please do not hesitate to contact us at michal.olejko@eveo.pl.

1.3. Limited Warranty

Eveo warrants this product to be free of defects in material and workmanship for a period of one year from date of buy.

During this warranty period eveo will repair or replace the defective unit in accordance with the following process:

A copy of the original invoice must be included when returning the defective unit to eveo. This limited warranty does not cover damages resulting from lightning or other power surges, misuse, abuse, abnormal conditions of operation, or attempts to alter or modify the function of the product.

This warranty is limited to the repair or replacement of the defective unit. In no event shall eveo be liable or responsible for any loss or damages, including but not limited to any lost profits, incidental or consequential damages, loss of business, or anticipatory profits arising from the use or inability to use this products.

Repairs made after the expiration of the warranty period are subject to a repair charge and the cost of return shipping. Please contact Eveo to arrange for any repair service and to obtain repair charge information.

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1 MINI3288 Introduction

1.1 Summary

MINI3288 is System On Module (SOM) based on the RK3288. The module have all pins function of RK3288, low cost and high performance. Compatible with MINI3288.

RK3288 Integrate quad-core Cortex-A17 with separately Neon and FPU coprocessor , also shared 1MB L2 Cache. More than 32bits address will support up to 8GB access space.

Currently, latest generation and most powerful GPU is embedded to support smoothly high-resolution (3840x2160) display and mainstream game. Support OpenVG1.1, OpenGL ES1.1/2.0/3.0, OpenCL1.1, RenderScript and DirectX11 etc. Full-format video decoder, including 4Kx2K multi-format decoder.

Lots of high-performance interface to get very flexible solution, such as multi-pipe display with dual-channel LVDS, MIPI-DSI or MIPI-CSI option, HDMI2.0 , dual-channel ISP embedded.

Dual-Channel 64bits DDR3/LPDDR2/LPDDR3 provide demanding memory bandwidths for high-performance and high-resolution application.

The single board computer has complete electronic documentation, schematics, demo applications, and third party industry-standard C compilers and embedded development environments for evaluation. We are sure to have the right single board computer for your applications.

1.2 RK3288 Features

•CPU

– Quad-Core Cortex-A17 Separately Integrated Neon and FPU per CPU 32KB/32KB L1 ICache/DCache per CPU Unified 1MB L2 Cache

– LPAE (Large Physical Address Extensions) , Support up to 8GB address space Virtualization Extensions Support

•GPU

– Quad-Core Mali-T7 series, latest powerful graphics processor Architected for GPU computing

– Support OpenGL ES1.1/2.0/3.0, OpenVG1.1, OpenCL1.1 and Renderscript , Directx11

•VPU

–Support MPEG-2,MPEG-4,AVS,VC-1,VP8,MVC with up to 1080p@60fps

–Support multi-format video decoder with up to 4Kx2K

–Support muti-format video encoder with up to 1080p@30fps

•Video Interface

–Video Input: camera , DVP

–Video display: 8/10bits LVDS, MIPI-DSI, HDMI2.0 to support maximum 4Kx2K display

•Memory Interface

–Nand Flash Interface

–eMMC Interface

–DDR interface

•Rich Connectivity

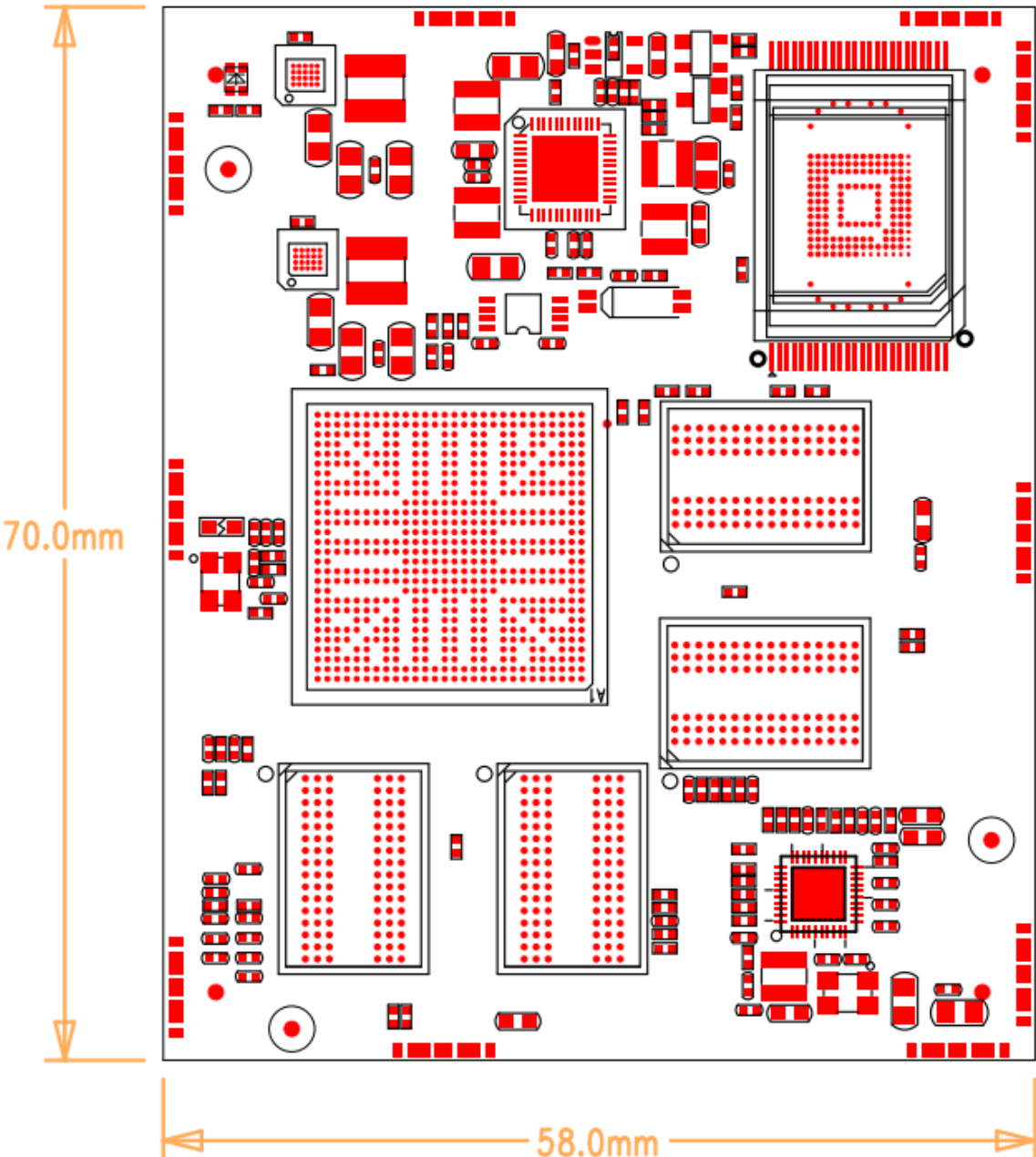
–3 SD/MMC/SDIO interface, compatible with SD3.0,SDIO3.0 and MMC4.5

- One 8-channels I2S/PCM interface, One 8-channels SPDIF interface
- One USB2.0 OTG , Two USB2.0 Host
- 100M/1000M RMII/RGMII Ethernet interface
- Dual channel TS stream interface, descramble and demux support
- Smart Card interface
- 4-CH UART, 2-CH SPI (option), 6-CH I2C(up to 4Mbps), 2-CH PWM (option)
- PS/2 master interface
- HSIC interface
- 3-CH ADC input

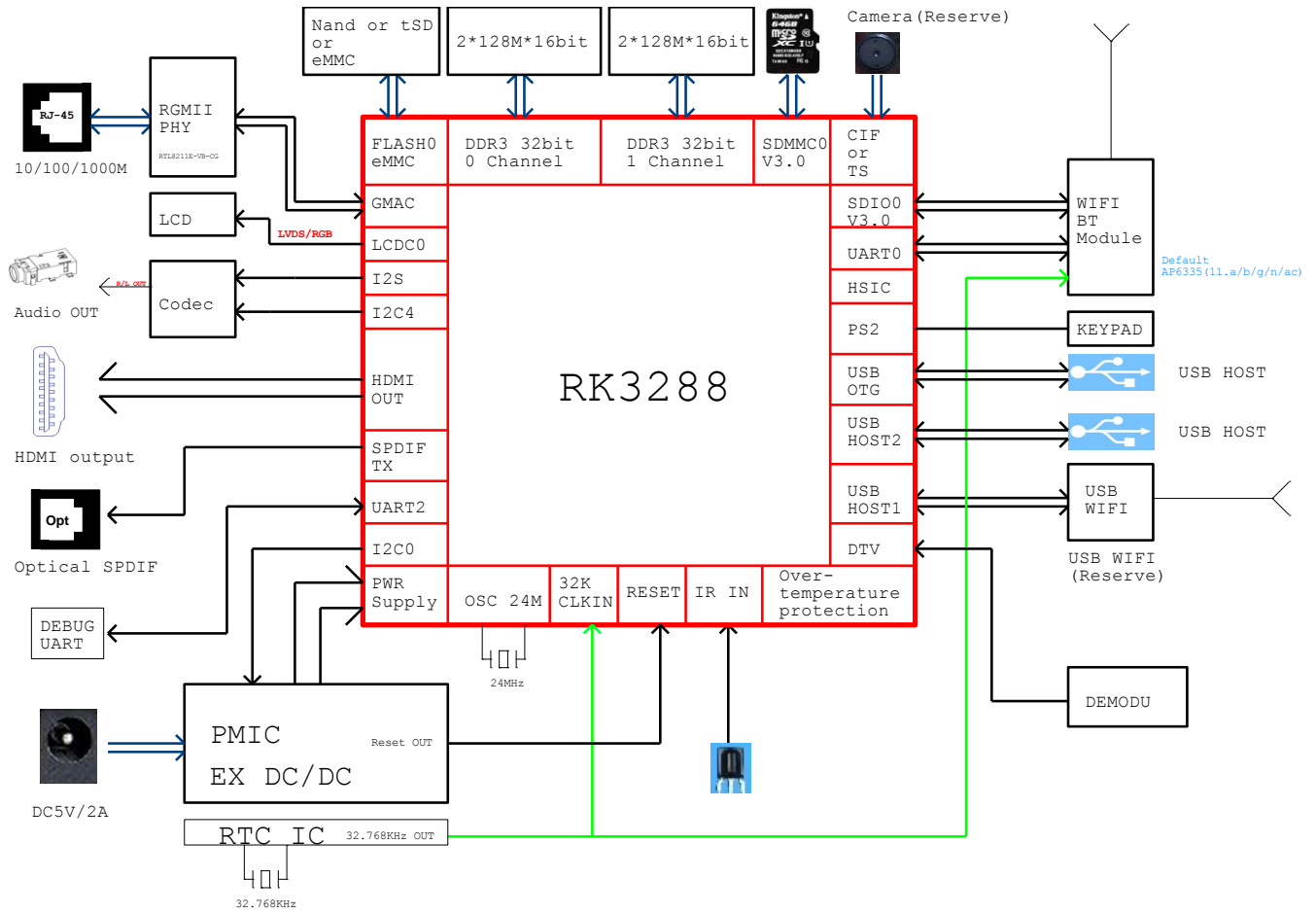
1.3 MINI3288 Features

Feature	Specifications
CPU	RK3288 Quad-core ARM Cortex-A17 MPCore processor
Memory	Default mounted 128M DDR3
NAND Flash	8GB eMMC Flash
Power	DC 3.6V-5V power supply
PMU	ACT8846
UART	4-CH (up to 5-CH, option by SPI0)
MIPI	MIPI CSI or DSI IF (Software selected)
LVDS	1-CH 10bit DuI-LVDS
Ethernet	1 Gigabit (RTL8211 on board)
USB	2-CH USB2.0 Host, 1-CH USB2.0 OTG
SPDF	1-CH
CIF	1-CH
HDMI	1-CH
PS2	1-CH
ADC	3-CH
PWM	2-CH (up to 4-CH, option by uart2)
IIC	5-CH
AUDIO IF	1-CH
SPI	2-CH
HSMC/SD	2-CH
System On Module Dimension	70mm x58mm

1.4 PCB Dimension



1.5 Block Diagram



1.6 CPU Module Introduction

The MINI3288 CPU board –MINI3288 is a full function Computer-on-Module based on RK3288 processor in small size and designed for embedded app

Header_A		Header_B	
TX_C	1	VCC_SYS	11
TX_0-	2	GND	12
TX_C+	3	VCC_SYS	13
TX_0+	4	GND	14
GND	5	nRESET	15
GND	6	MDI0-	16
TX_1-	7	MDI0+	17
TX_2-	8	MDI1-	18
TX_1+	9	MDI1+	19
TX_2+	10	MDI2-	20
HDMI_HPD	11	MDI2+	21
HDMI_CEC	12	MDI3-	22
I2C5_SDA_HDMI	13	MDI3+	23
I2C5_SCL_HDMI	14	MDI4-	24
GND	15	MDI4+	25
LCD_VSYNC	16	GND	26
LCD_VSYNC	17	RST_KEY	27
LCD_CLK	18	SDIO0_CMD	28
LCD_DEN	19	SDIO0_DO	29
LCD_D0_LD0P	20	SDIO0_D1	30
LCD_D1_LD0N	21	SDIO0_D2	31
LCD_D2_LD1P	22	SDIO0_D3	32
LCD_D3_LD1N	23	SDIO0_CLK	33
LCD_D4_LD2P	24	BT_WAKE	34
LCD_D5_LD2N	25	SDIO0_WP	35
LCD_D6_LD3P	26	WIFI_HOST_WAKE	36
LCD_D7_LD3N	27	BT_HOST_WAKE	37
LCD_D8_LD4P	28	BT_RST	38
LCD_D9_LD4N	29	BT_HOST_WAKE	39
LCD_D10_LCK0P	30	SPDIF_TX	40
LCD_D11_LCK0N	31	SPDIF_RX	41
LCD_D12_LD5P	32	SPDIF_TX	42
LCD_D13_LD5N	33	SPDIF_RX	43
LCD_D14_LD6P	34	OTG_VBUS_DRV	44
LCD_D15_LD6N	35	HOST_VBUS_DRV	45
LCD_D16_LD7P	36	UART0_RX	46
LCD_D17_LD7N	37	GND	47
LCD_D18_LD8P	38	UART0_TX	48
LCD_D19_LD8N	39	GND	49
LCD_D20_LD9P	40	UART1_CTSn	50
LCD_D21_LD9N	41	UART1_RTSn	51
LCD_D22_LCK1P	42	UART1_RX_TSo_Do	52
LCD_D23_LCK1N	43	UART1_TX	53
GND	44	TSo_CLK	54
MIPI_TXRX_CLKN	45	TSo_VALID	55
MIPI_TXRX_D0P	46	TSo_ERR	56
MIPI_TXRX_D0N	47	GPI07_B4_U	57
MIPI_TXRX_D1P	48	GPI07_B4_D	58
MIPI_TXRX_D1N	49	GND	59
MIPI_TXRX_D2P	50	SDMMC_CMD	60
MIPI_TXRX_D2N	51	SDMMC_D0	61
MIPI_TXRX_D3P	52	SDMMC_D1	62
MIPI_TXRX_D3N	53	SDMMC_D2	63
GND	54	SDMMC_D3	64
MIPI_TXRX_D4P	55	SDMMC_CLK	65
MIPI_TXRX_D4N	56	GND	66
MIPI_TXRX_D5P	57	SDMMC_D0	67
MIPI_TXRX_D5N	58	SDMMC_CMD	68
GND	59	SDMMC_D1	69
VCC_18	60	SDMMC_D2	70
VCC_18	61	SDMMC_D3	71
VCC_18	62	SDMMC_PWR	72
VCC_18	63	GPI00_B5_D	73
VCC_18	64	GND	74
VCC_18	65	GPI07_B7_U	75
VCC_18	66	I2S_SDI	76
VCC_18	67	I2S_MCLK	77
VCC_18	68	I2S_SCLK	78
VCC_18	69	I2S_LRCK_RX	79
VCC_18	70	I2S_LRCK_TX	80
VCC_18	71	I2S_SDO0	81
VCC_18	72	I2S_SDO1	82
VCC_18	73	I2S_SDO2	83
VCC_18	74	I2S_SDO3	84
VCC_18	75	SPDIF_TX	85
VCC_18	76	I2C2_SDA	86
VCC_18	77	GND	87
VCC_18	78	I2C1_SDA	88
VCC_18	79	I2C2_SCL	89
VCC_18	80	I2C4_SDA	90
VCC_18	81	I2C1_SCL	91
VCC_18	82	UART2_RX	92
VCC_18	83	I2C4_SCL	93
VCC_18	84	UART2_TX	94
VCC_18	85	UART3_RX	95
VCC_18	86	UART3_TX	96
VCC_18	87	UART3_CTSn	97
VCC_18	88	UART3_RTSn	98
VCC_18	89	PWR_KEY	99
VCC_18	90	GPI07_CS_D	100
VCC_18	91		
VCC_18	92		
VCC_18	93		
VCC_18	94		
VCC_18	95		
VCC_18	96		
VCC_18	97		
VCC_18	98		
VCC_18	99		
VCC_18	100		

Legend: MIPI (orange), POWER (red), GND (black), UART (purple), LCD (blue), SDIO (green), GPIO (brown), I2C (dark blue), I2S (cyan), SPI (grey), SPI2 (light blue), CIF (pink)

Board Dimension

* Board size: 70mm x58mm

* Pin number: 200pins

* Layer: 8 Layers

Electric property

◆ Dissipation

Symbol	Parameter	Min	Typ	Max	Unit
SYS_POWER	System Supply Voltage Input	3.6	5	5	V
VCC_IO	IO Supply Voltage Output		3.3		V
VCCA_18	RK1000-S		1.8		V
VCCA_33	LCDC/I2S Controller		3.3		V
VCC_18	RK3288 SAR-ADC/ RK3288 USB PHY		1.8		V
VCC_LAN	LAN PHY		3.3		V
VCC_RTC	RTC Battery Voltage	2.5	3	3.6	V
I _{sys} _power	System Supply Max Current		1.1	1.5	A
I _{max} (VCC_IO)	VCC_IO Max Current		600	800	mA
I _{vcca_18}	VCCA_18 Max Current			250	mA
I _{vcca_33}	VCCA_33 Max Current			350	mA
I _{vcc_18}	VCC_18 Max Current			350	mA
I _{rtc}	RTC Input Current			10	uA

◆ CPU Temperature

Test Conditions	Environment Temperature	Min	Typ	Max	Unit
Standby	20		43	45	°C
Play the video	20		45	48	°C
Full power	20		80	85	°C

Pin Definition

J1

Pin	Signal name	Fuction 1	Fuction 2	IO Type
1	TX_C-	HDMI TMDS Clock-		O
2	TX_0-	HDMI TMDS Data0-		O
3	TX_C+	HDMI TMDS Clock+		O
4	TX_0+	HDMI TMDS Data0+		O
5	GND	Power Ground		P
6	GND	Power Ground		P
7	TX_1-	HDMI TMDS Data1-		O
8	TX_2-	HDMI TMDS Data2-		O
9	TX_1+	HDMI TMDS Data1+		O
10	TX_2+	HDMI TMDS Data2+		O
11	HDMI_HPD	HDMI Hot Plug Detection		I

12	HDMI_CEC	HDMI Consumer Electronics Control	GPIO7_C0_u	I/O
13	I2C5_SDA_HDMI	I2C5 Bus Data	GPIO7_C3_u	I/O
14	I2C5_SCL_HDMI	I2C5 Bus Clock	GPIO7_C4_u	I/O
15	GND	Power Ground		P
16	LCD_VSYNC	LCD Vertical Synchronization	GPIO1_D1_d	I/O
17	LCD_HSYNC	LCD Horizontal Synchronization	GPIO1_D0_d	I/O
18	LCD_CLK	LCD Clock	GPIO1_D3_d	I/O
19	LCD_DEN	LCD Enable	GPIO1_D2_d	I/O
20	LCD_D0_LD0P	LCD Data0 or LVDS Differential Data0+		I/O
21	LCD_D1_LD0N	LCD Data1 or LVDS Differential Data0-		I/O
22	LCD_D2_LD1P	LCD Data2 or LVDS Differential Data1+		I/O
23	LCD_D3_LD1N	LCD Data3 or LVDS Differential Data1-		I/O
24	LCD_D4_LD2P	LCD Data4 or LVDS Differential Data2+		I/O
25	LCD_D5_LD2N	LCD Data5 or LVDS Differential Data2-		I/O
26	LCD_D6_LD3P	LCD Data6 or LVDS Differential Data3+		I/O
27	LCD_D7_LD3N	LCD Data7 or LVDS Differential Data3-		I/O
28	LCD_D8_LD4P	LCD Data8 or LVDS Differential Data4+		I/O
29	LCD_D9_LD4N	LCD Data9 or LVDS Differential Data4-		I/O
30	LCD_D10_LCK0P	LCD Data10 or LVDS Differential Clock0+		I/O
31	LCD_D11_LCK0N	LCD Data11 or LVDS Differential Clock0-		I/O
32	LCD_D12_LD5P	LCD Data12 or LVDS Differential Data5+		I/O
33	LCD_D13_LD5N	LCD Data13 or LVDS Differential Data5-		I/O
34	LCD_D14_LD6P	LCD Data14 or LVDS Differential Data6+		I/O
35	LCD_D15_LD6N	LCD Data15 or LVDS Differential Data6-		I/O
36	LCD_D16_LD7P	LCD Data16 or LVDS Differential Data7+		I/O
37	LCD_D17_LD7N	LCD Data17 or LVDS Differential Data7-		I/O
38	LCD_D18_LD8P	LCD Data18 or LVDS Differential Data8+		I/O
39	LCD_D19_LD8N	LCD Data19 or LVDS Differential Data8-		I/O
40	LCD_D20_LD9P	LCD Data20 or LVDS Differential Data9-		I/O
41	LCD_D21_LD9N	LCD Data21 or LVDS Differential Data9+		I/O
42	LCD_D22_LCK1P	LCD Data22 or LVDS Differential Clock1+		I/O
43	LCD_D23_LCK1N	LCD Data23 or LVDS Differential Clock1-		I/O
44	GND	Power Ground		P
45	MIPI_TX/RX_CLKN	MIPI Clock negative signal		I/O
46	MIPI_TX/RX_D0P	MIPI data pair 0 positive signal		I/O
47	MIPI_TX/RX_CLKP	MIPI Clock positive signal		I/O
48	MIPI_TX/RX_D0N	MIPI data pair 0 negative signal		I/O
49	MIPI_TX/RX_D2N	MIPI data pair 2 negative signal		I/O
50	MIPI_TX/RX_D1N	MIPI data pair 1 negative signal		I/O
51	MIPI_TX/RX_D2P	MIPI data pair 2 positive signal		I/O

52	MIPI_TX/RX_D1P	MIPI data pair 1 positive signal		I/O
53	MIPI_TX/RX_D3P	MIPI data pair 3 positive signal		I/O
54	GND	Power Ground		P
55	MIPI_TX/RX_D3N	MIPI data pair 3 negative signal		I/O
56	DVP_PWR		GPIO0_C1_d	I/O
57	HSIC_STROBE	HSIC_STROBE		
58	HSIC_DATA	HSIC_DATA		
59	GND	Power Ground		P
60	CIF_D1		GPIO2_B5_d	I/O
61	CIF_D0		GPIO2_B4_d	I/O
62	CIF_D3	HOST_D1 or TS_D1	GPIO2_A1_d	I/O
63	CIF_D2	HOST_D0 or TS_D0	GPIO2_A0_d	I/O
64	CIF_D5	HOST_D3 or TS_D3	GPIO2_A3_d	I/O
65	CIF_D4	HOST_D2 or TS_D2	GPIO2_A2_d	I/O
66	CIF_D7	HOST_CKINN or TS_D5	GPIO2_A5_d	I/O
67	CIF_D6	HOST_CKINP or TS_D4	GPIO2_A4_d	I/O
68	CIF_D9	HOST_D5 or TS_D7	GPIO2_A7_d	I/O
69	CIF_D8	HOST_D4 or TS_D6	GPIO2_A6_d	I/O
70	CIF_PDNO		GPIO2_B7_d	I/O
71	CIF_D10		GPIO2_B6_d	I/O
72	CIF_HREF	HOST_D7 or TS_VALID	GPIO2_B1_d	I/O
73	CIF_VSYNC	HOST_D6 or TS_SYNC	GPIO2_B0_d	I/O
74	CIF_CLKOUT	HOST_WKREQ or TS_FAIL	GPIO2_B3_d	I/O
75	CIF_CLKIN	HOST_WKACK or GPS_CLK or TS_CLKOUT	GPIO2_B2_d	I/O
76	I2C3_SCL		GPIO2_C0_u	I/O
77	I2C3_SDA		GPIO2_C1_u	I/O
78	GND	Power Ground		P
79	GPIO0_B2_D	OTP_OUT	GPIO0_B2_d	I/O
80	GPIO7_A3_D		GPIO7_A3_d	I/O
81	GPIO7_A6_U		GPIO7_A6_u	I/O
82	GPIO0_A6_U		GPIO0_A6_u	I/O
83	LED0_AD0	PHYAD0		
84	LED1_AD1	PHYAD1		
85	VCC_LAN	Ethernet Power Supply 3.3V		
86	PS2_DATA	PS2 Data	GPIO8_A1_u	I/O
87	PS2_CLK	PS2 Clock	GPIO8_A0_u	I/O
88	ADC0_IN			I
89	GPIO0_A7_U		PMUGPIO0_A7_u	I/O
90	ADC1_IN	RECOVER		I
91	VCCIO_SD	SD Card power Supply 3.3V		

92	ADC2_IN			I
93	VCCA_18	Power 1.8V		
94	VCCA_33	Power 3.3V		
95	VCC_18	Power 1.8V		
96	VCC_RTC	Real-Time Clock Power Supply		
97	VCC_IO	3.3V		
98	GND	Power Ground		P
99	VCC_IO	3.3V		
100	GND	Power Ground		P

J2

Pin	Signal name	Fuction 1	Fuction 2	IO Type
1	VCC_SYS	System Power Supply 3.6~5V		
2	GND	Power Ground		
3	VCC_SYS	System Power Supply 3.6~5V		
4	GND	Power Ground		
5	nRESET	System Reset		I
6	MDIO+	100M/1G Ethernet MDIO+		
7	MDI1+	100M/1G Ethernet MDI1+		
8	MDIO-	100M/1G Ethernet MDIO-		
9	MDI1-	100M/1G Ethernet MDI1-		
10	IR_INT	PWM CH0	GPIO7_A0_d	I/O
11	MDI2+	100M/1G Ethernet MDI2+		
12	MDI3+	100M/1G Ethernet MDI3+		
13	MDI2-	100M/1G Ethernet MDI2-		
14	MDI3-	100M/1G Ethernet MDI3-		
15	GND	Power Ground		P
16	RST_KEY	System Reset		I
17	SDIO0_CMD		GPIO4_D0_u	I/O
18	SDIO0_D0		GPIO4_C4_u	I/O
19	SDIO0_D1		GPIO4_C5_u	I/O
20	SDIO0_D2		GPIO4_C6_u	I/O
21	SDIO0_D3		GPIO4_C7_u	I/O
22	SDIO0_CLK		GPIO4_D1_d	I/O
23	BT_WAKE	SDIO0_DET	GPIO4_D2_u	I/O
24	SDIO0_WP		GPIO4_D3_d	I/O
25	WIFI_REG_ON	SDIO0_PWR	GPIO4_D4_d	I/O
26	BT_HOST_WAKE		GPIO4_D7_u	I/O
27	WIFI_HOST_WAKE	SDIO0_INTn	GPIO4_D6_u	I/O
28	BT_RST	SDIO0_BKPWR	GPIO4_D5_d	I/O

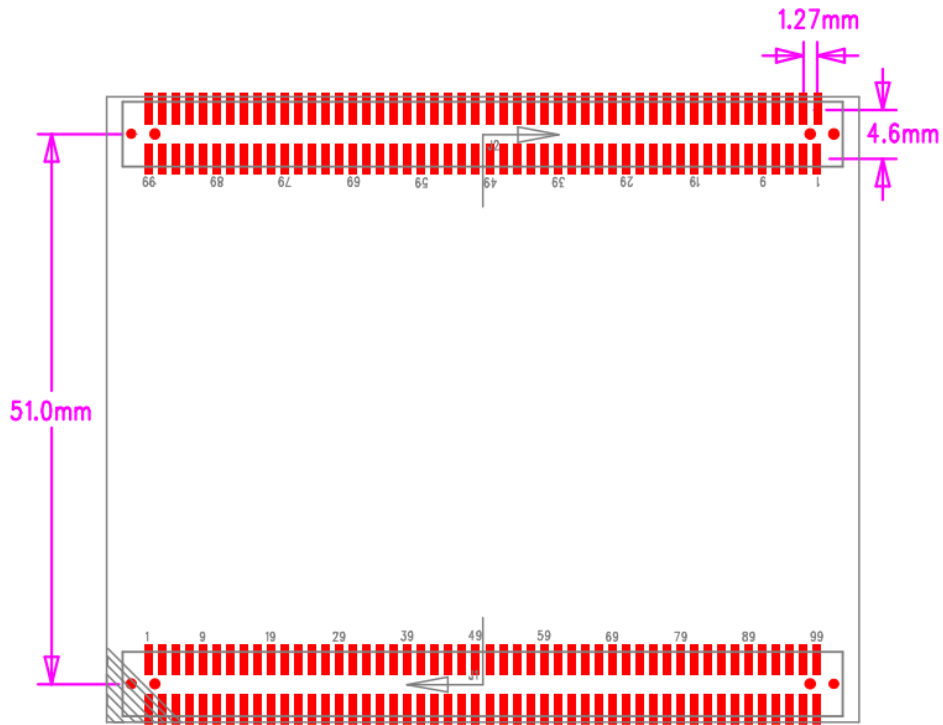
29	SPI2_CLK	SC_IO_T1	GPIO8_A6_d	I/O
30	SPI2_CSn0	SC_DET_T1	GPIO8_A7_u	I/O
31	SPI2_RXD	SC_RST_T1	GPIO8_B0_d	I/O
32	SPI2_TXD	SC_CLK_T1	GPIO8_B1_d	I/O
33	OTG_VBUS_DRV		GPIO0_B4_d	I/O
34	HOST_VBUS_DRV		GPIO0_B6_d	I/O
35	UART0_RX		GPIO4_C0_u	I/O
36	UART0_TX		GPIO4_C1_d	I/O
37	GND	Power Ground		P
38	UART0_CTS		GPIO4_C2_u	I/O
39	OTG_DM			
40	UART0_RTS		GPIO4_C3_u	I/O
41	OTG_DP			
42	OTG_ID			
43	HOST1_DM	USB host port 1 negative data		
44	OTG_DET			
45	HOST1_DP	USB host port 1 positive data		
46	HOST2_DM	USB host port 2 negative data		
47	SPI0_CSn0	UART4_RTSn or TS0_D5	GPIO5_B5_u	I/O
48	HOST2_DP	USB host port 2 positive data		
49	SPI0_CLK	UART4_CTSn or TS0_D4	GPIO5_B4_u	I/O
50	GND	Power Ground		P
51	SPI0_UART4_RXD	UART4_RX or TS0_D7	GPIO5_B7_u	I/O
52	SPI0_UART4_TXD	UART4_TX or TS0_D6	GPIO5_B6_d	I/O
53	GND	Power Ground		P
54	TS0_SYNC	SPI0_CSn1	GPIO5_C0_u	I/O
55	UART1_CTSn	TS0_D2	GPIO5_B2_u	I/O
56	UART1_RTSn	TS0_D3	GPIO5_B3_u	I/O
57	UART1_RX_TS0_D0	TS0_D0	GPIO5_B0_u	I/O
58	UART1_TX	TS0_D1	GPIO5_B1_d	I/O
59	TS0_CLK		GPIO5_C2_d	I/O
60	TS0_VALID		GPIO5_C1_d	I/O
61	TS0_ERR		GPIO5_C3_d	I/O
62	GPIO7_B4_U	ISP_SHUTTEREN or SPI1_CLK	GPIO7_B4_u	I/O
63	SDMMC_CLK	JTAG_TDO	GPIO6_C4_d	I/O
64	GND	Power Ground		P
65	SDMMC_D0	JTAG_TMS	GPIO6_C0_u	I/O
66	SDMMC_CMD		GPIO6_C5_u	I/O
67	SDMMC_D2	JTAG_TDI	GPIO6_C2_u	I/O
68	SDMMC_D1	JTAG_TRSTN	GPIO6_C1_u	I/O

69	SDMMC_DET		GPIO6_C6_u	I/O
70	SDMMC_D3	JTAG_TCK	GPIO6_C3_u	I/O
71	SDMMC_PWR	eDP_HOTPLUG	GPIO7_B3_d	I/O
72	GPIO0_B5_D	General IO		I/O
73	GND	Power Ground		P
74	GPIO7_B7_U	ISP_SHUTTERTRIG	GPIO7_B7_u	I/O
75	I2S_SDI		GPIO6_A3_d	I/O
76	I2S_MCLK		GPIO6_B0_d	I/O
77	I2S_SCLK		GPIO6_A0_d	I/O
78	I2S_LRCK_RX		GPIO6_A1_d	I/O
79	I2S_LRCK_TX		GPIO6_A2_d	I/O
80	I2S_SDO0		GPIO6_A4_d	I/O
81	I2S_SDO1		GPIO6_A5_d	I/O
82	I2S_SDO2		GPIO6_A6_d	I/O
83	I2S_SDO3		GPIO6_A7_d	I/O
84	SPDIF_TX		GPIO6_B3_d	I/O
85	I2C2_SDA		GPIO6_B1_u	I/O
86	GND	Power Ground		P
87	I2C1_SDA	SC_RST	GPIO8_A4_u	I/O
88	I2C2_SCL		GPIO6_B2_u	I/O
89	I2C4_SDA		GPIO7_C1_u	I/O
90	I2C1_SCL	SC_CLK	GPIO8_A5_u	I/O
91	UART2_RX	IR_RX or PWM2	GPIO7_C6_u	I/O
92	I2C4_SCL		GPIO7_C2_u	I/O
93	UART3_RX	GPS_MAG or HSADC_D0_T1	GPIO7_A7_u	I/O
94	UART2_TX	IR_TX or PWM3 or EDPHDMI_CEC	GPIO7_C7_u	I/O
95	UART3_RTSn		GPIO7_B2_u	I/O
96	UART3_TX	GPS_SIG or HSADC_D1_T1	GPIO7_B0_d	I/O
97	PWM1		GPIO7_A1_d	I/O
98	UART3_CTSn	GPS_RFCLK or GPS_CLK_T1	GPIO7_B1_u	I/O
99	PWR_KEY			I
100	GPIO7_C5_D		GPIO7_C5_d	I/O

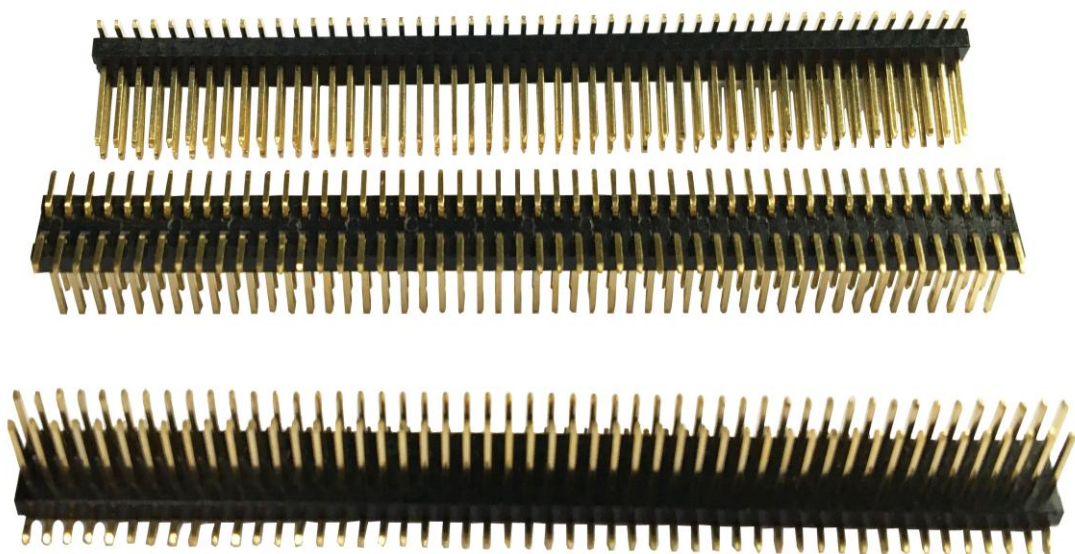
2 How to use the MINI3288 module

2.1 Connectors

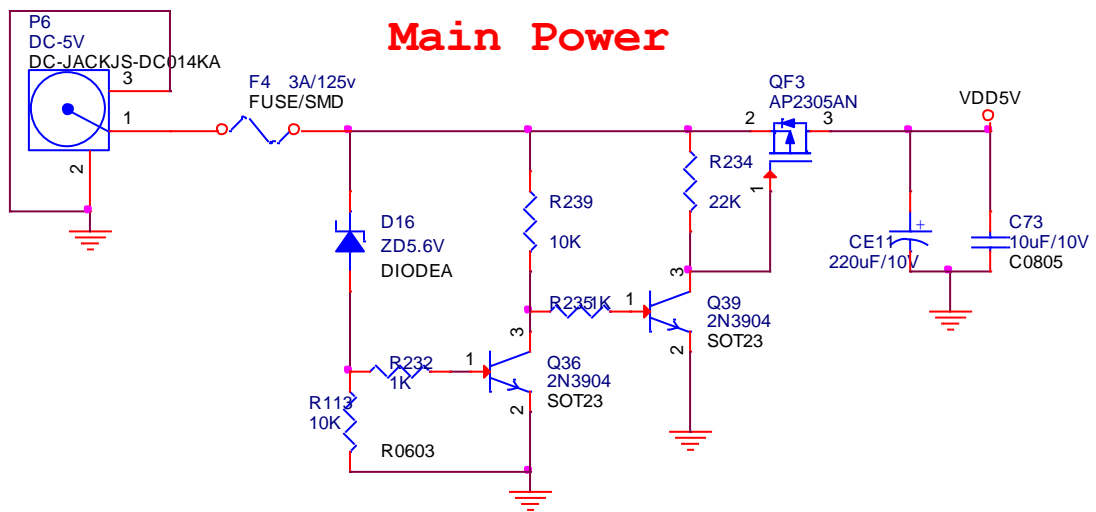
◆ *PCB dimension of the connectors*



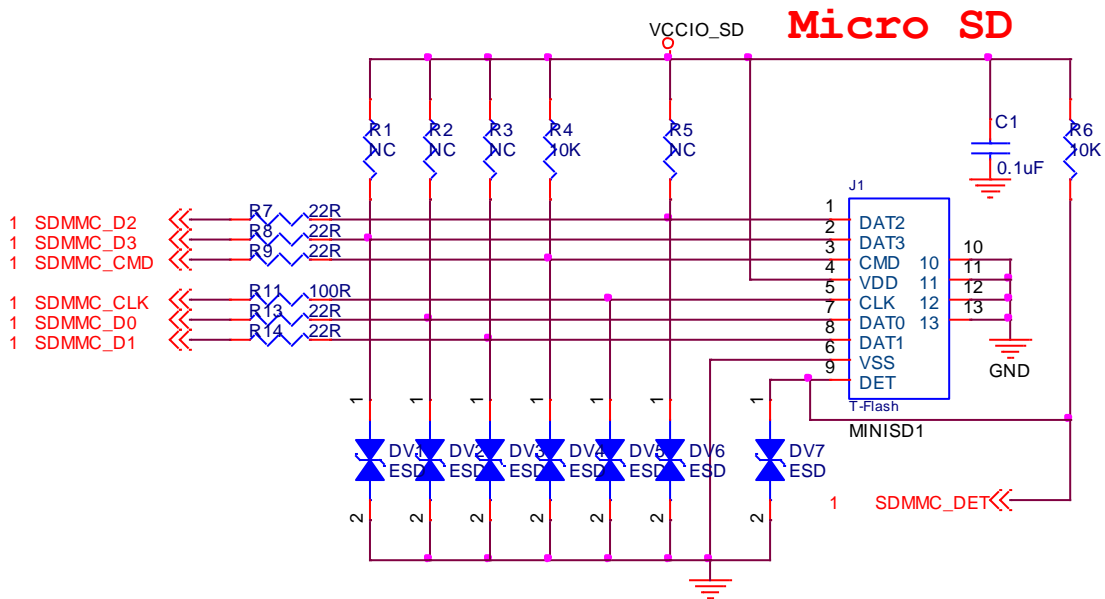
◆ *The picture of the connectors*



2.4 POWER circuit

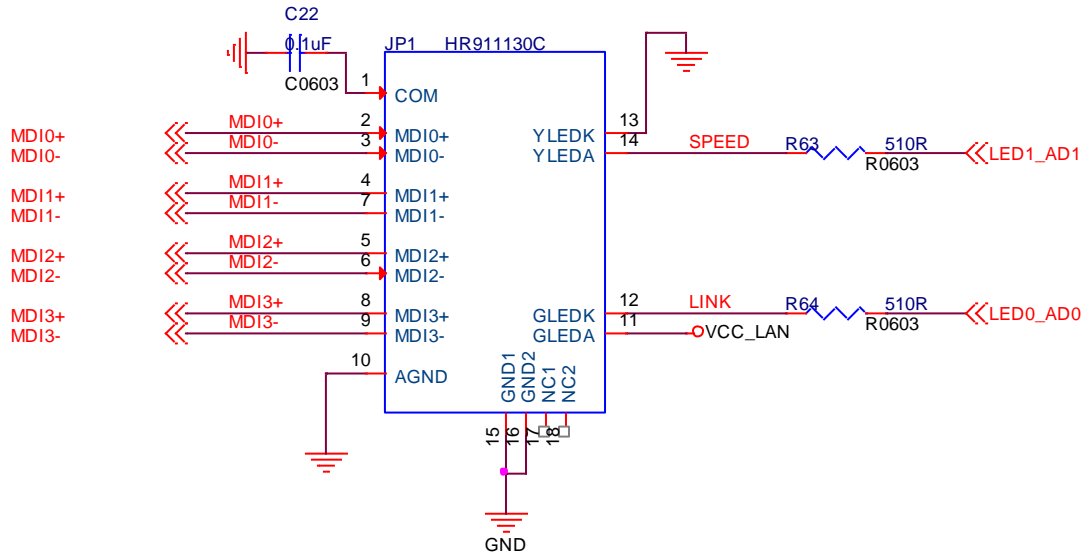


2.5 SD interface circuit

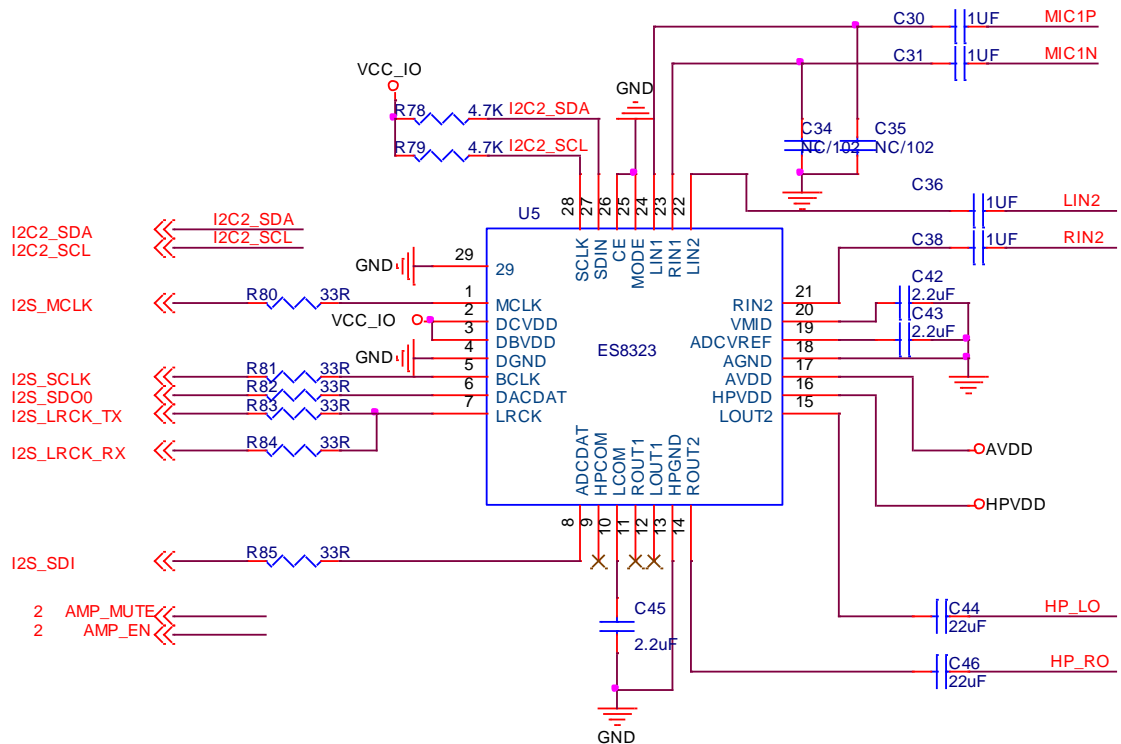


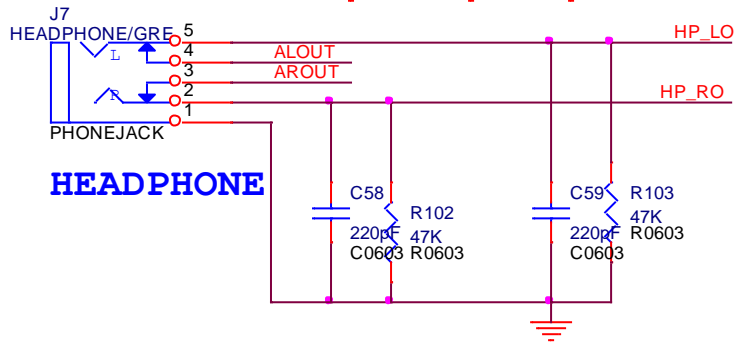
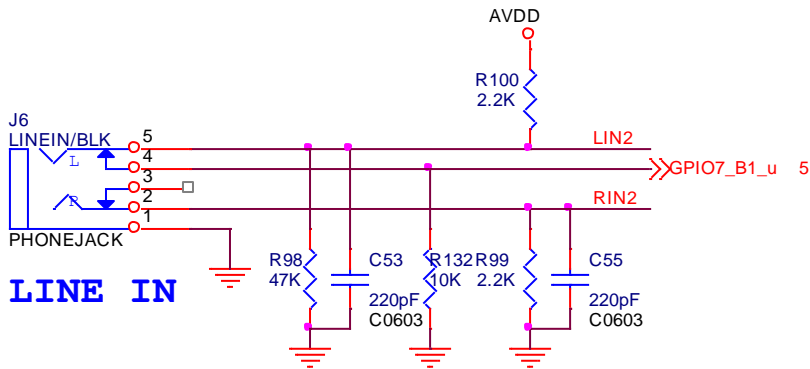
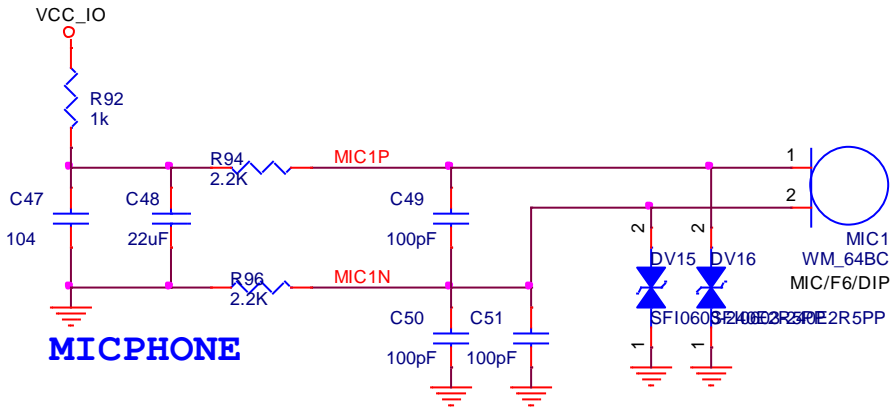
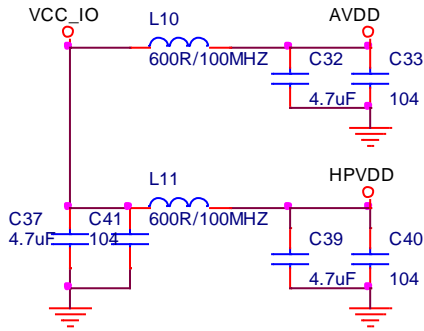
SD (Security Digital) card is a kind of widely applied card. A specified interface circuit on platform supports reading and writing function of SD card.

2.6 Ethernet interface circuit

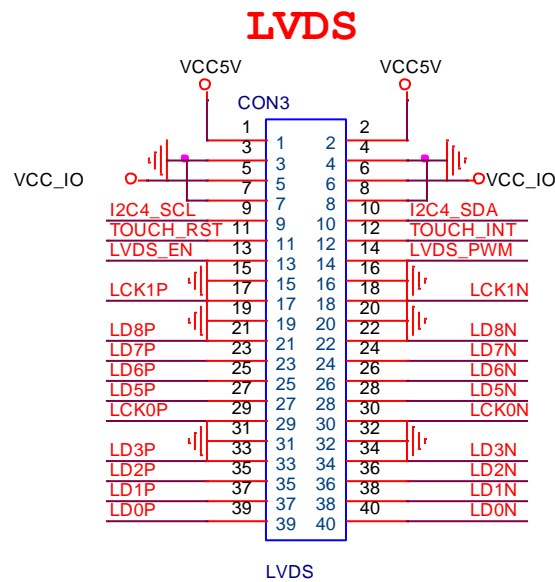
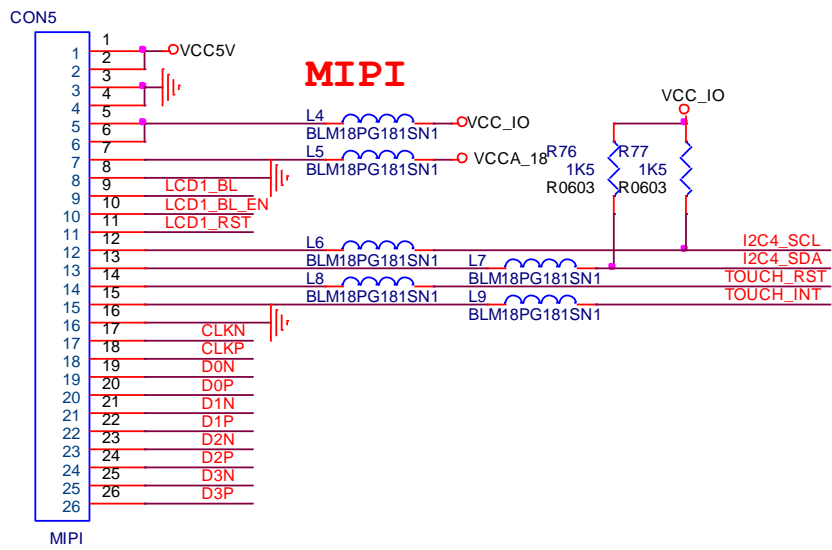


2.7 Audio codec circuit

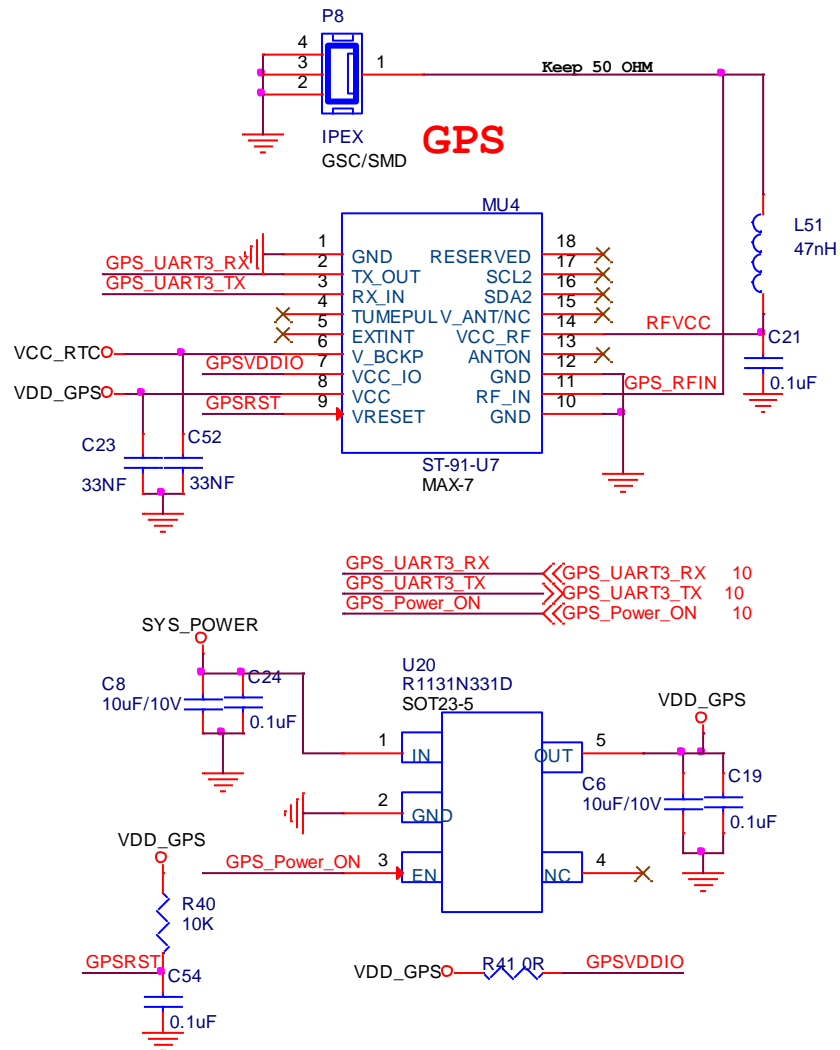




2.8 Display circuit

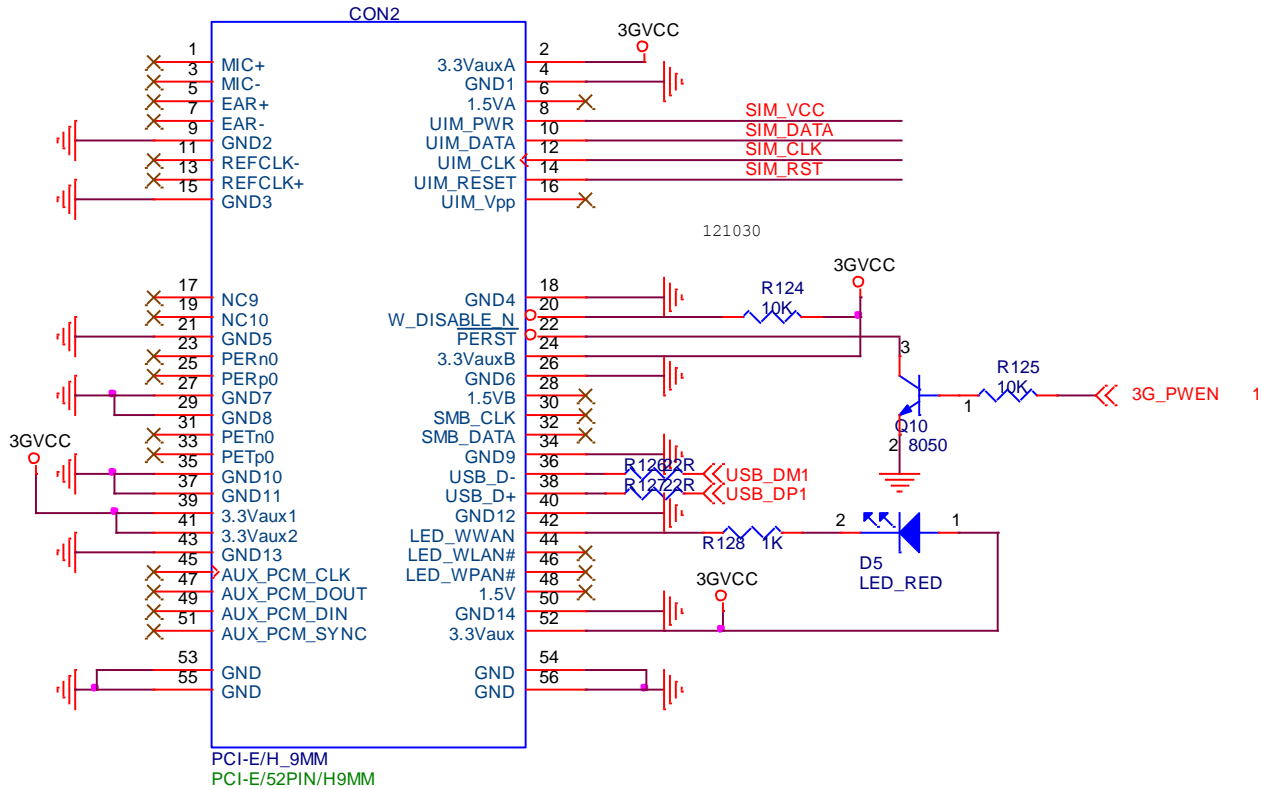


2.11 GPS circuit

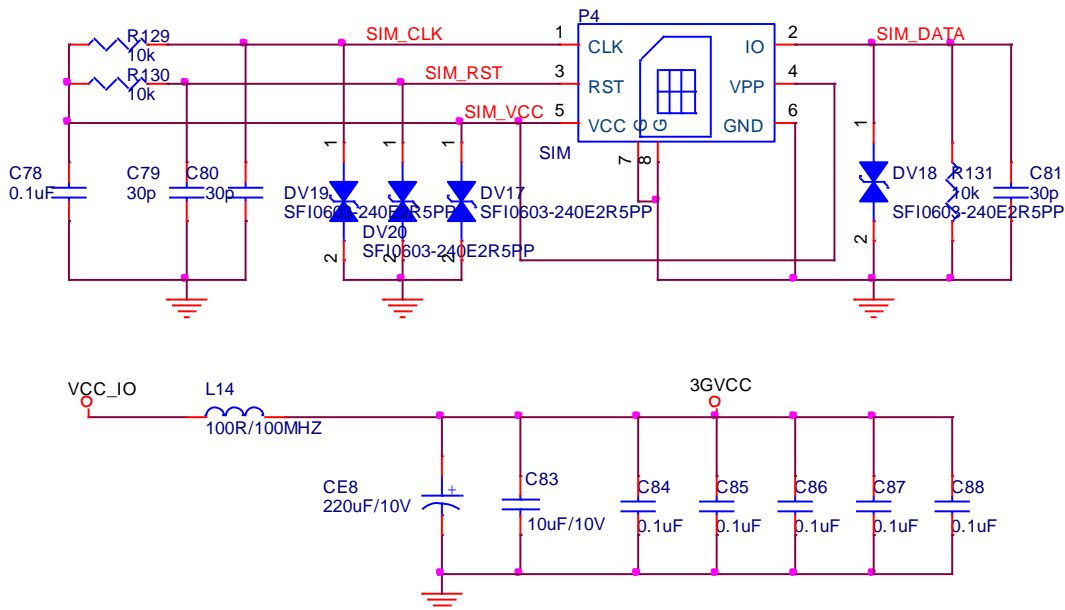


2.12 3G circuit

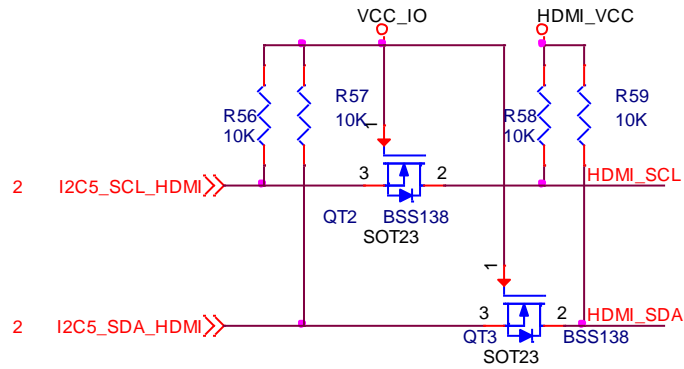
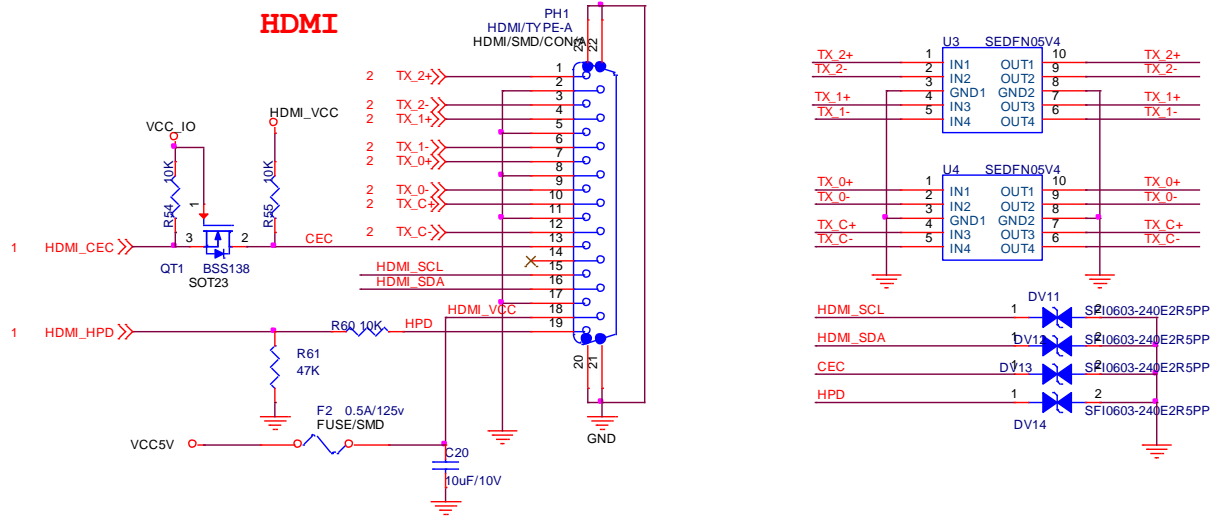
MINI PCI-E CON



SIM



2.13 HDMI circuit



I2C Level Shift